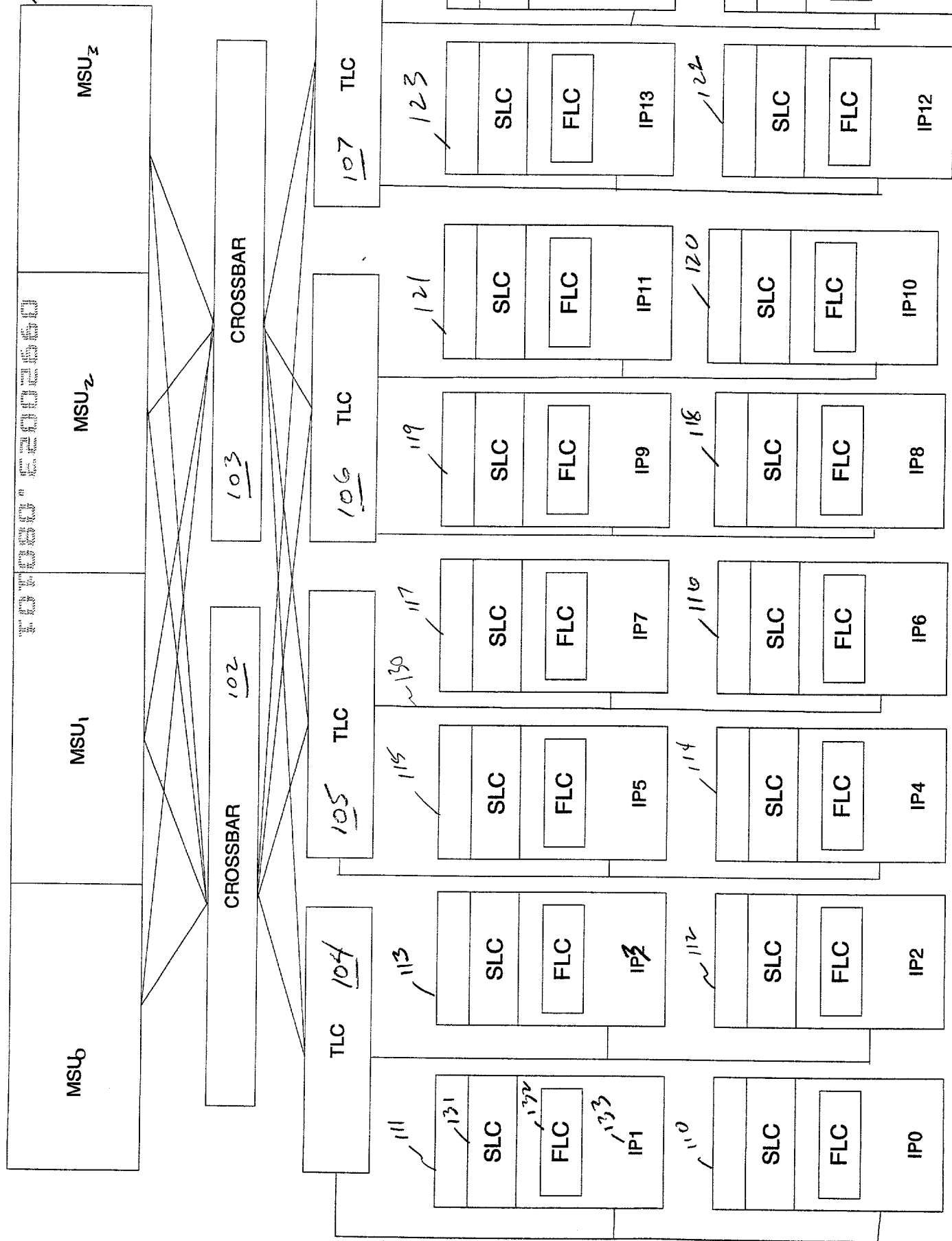


Fig. 1.

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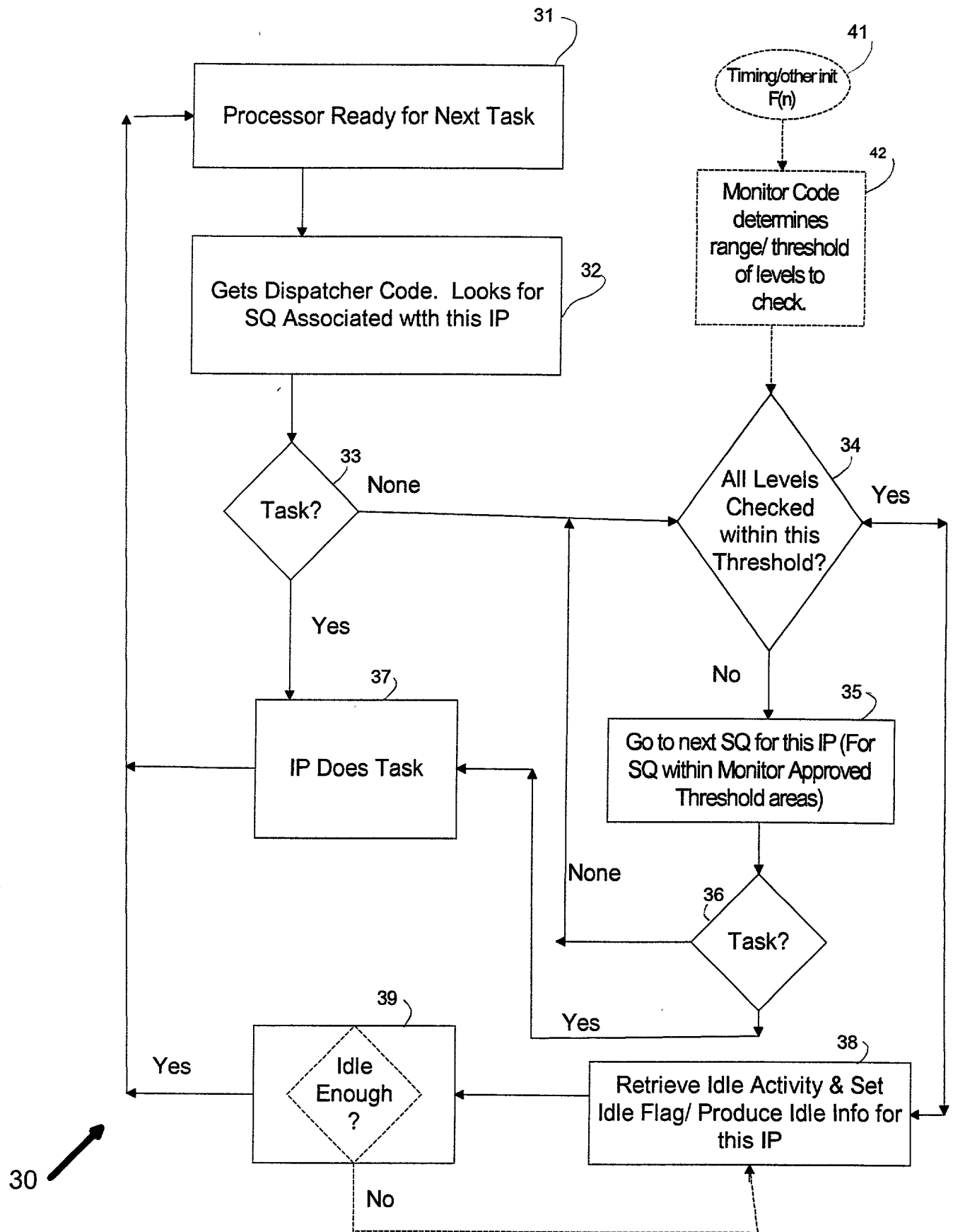


Fig. 3

Load Balancing Level

IP

	0	1	2	3	4
0	0	1	2	4	8
1	1	0	3	5	9
2	2	3	0	6	10
3	3	2	1	7	11
4	4	5	6	0	12
5	5	4	7	1	13
6	6	7	4	2	14
7	7	6	5	3	15
8	8	9	10	12	0
9	9	8	11	13	1
10	10	11	8	14	2
11	11	10	9	15	3
12	12	13	14	8	4
13	13	12	15	9	5
14	14	15	12	10	6
15	15	14	13	11	7

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Fig. 4

Downloaded from www.scribd.com

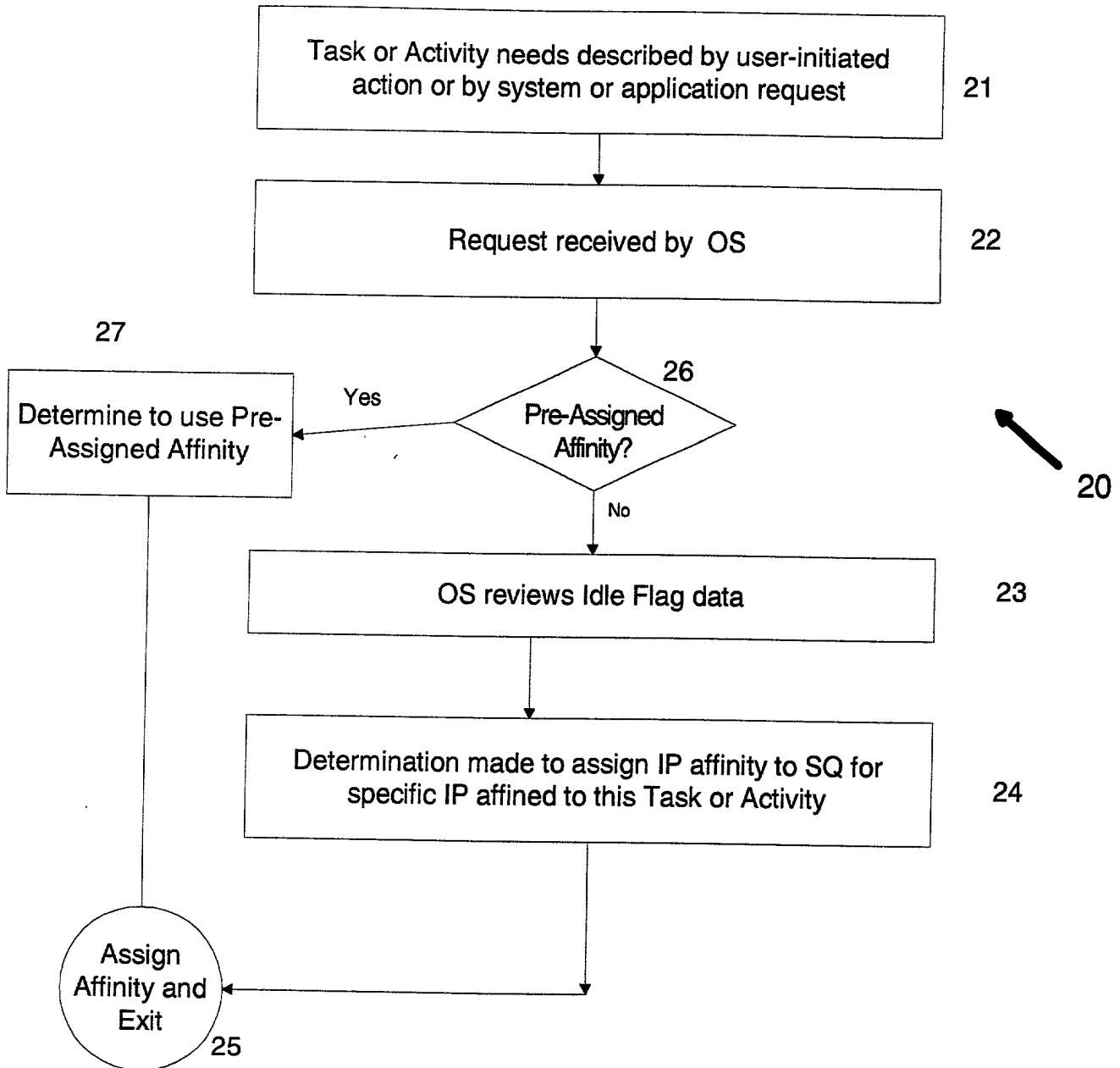


Fig. 2

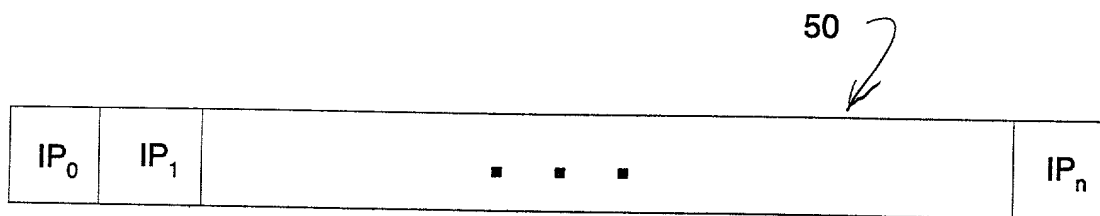


Fig. 5 (IP IDLE FLAG DATA)

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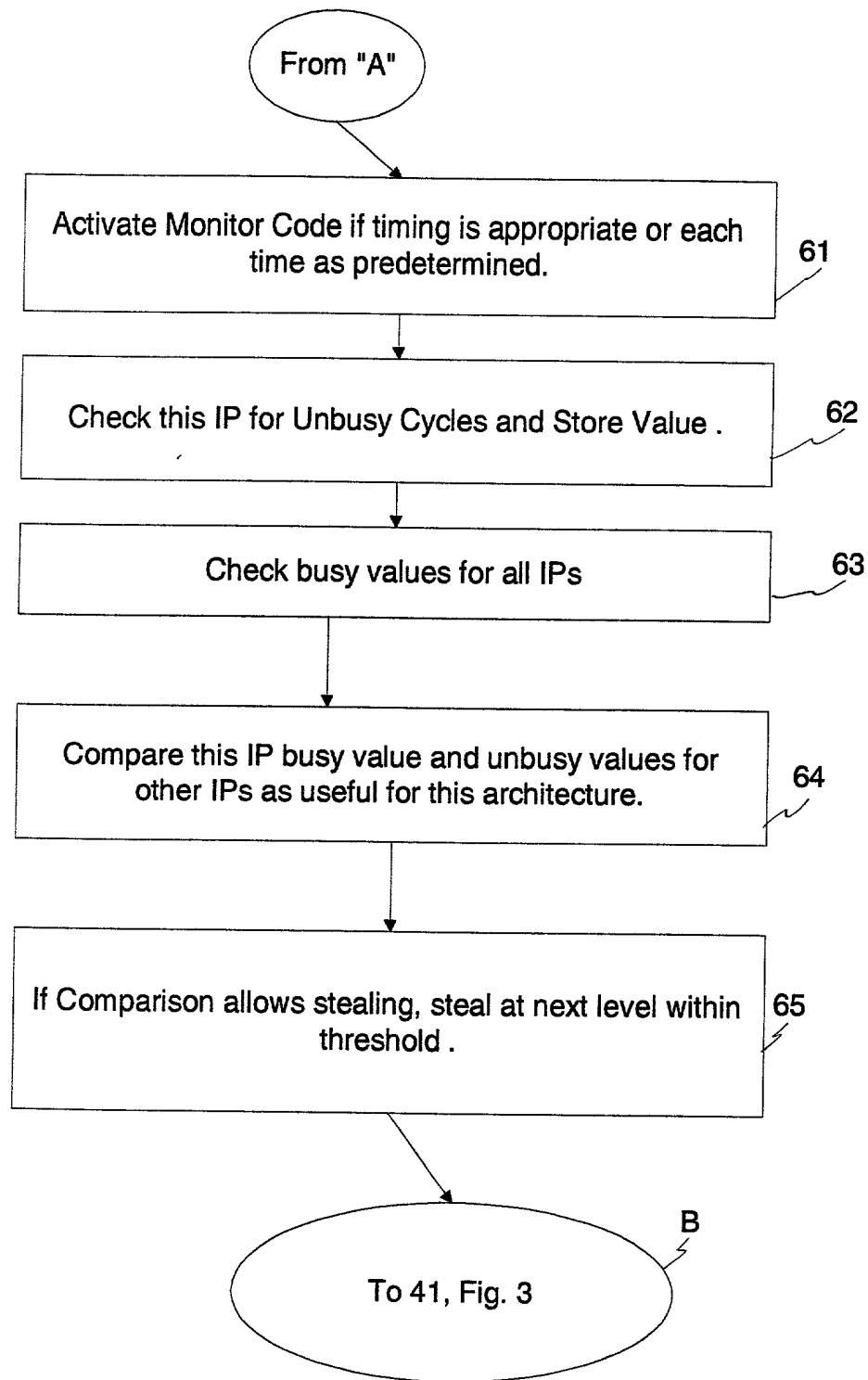


Fig. 6

The graph illustrates a step function where the vertical axis represents levels L0, L1, L2, L3, and L4, and the horizontal axis represents IP Idleness. The function starts at L0, then steps up to L1, then to L2, then to L3, and finally to L4. An arrow labeled 'System Idleness' points upwards, and an arrow labeled 'IP Idleness' points to the right.

Fig. 7